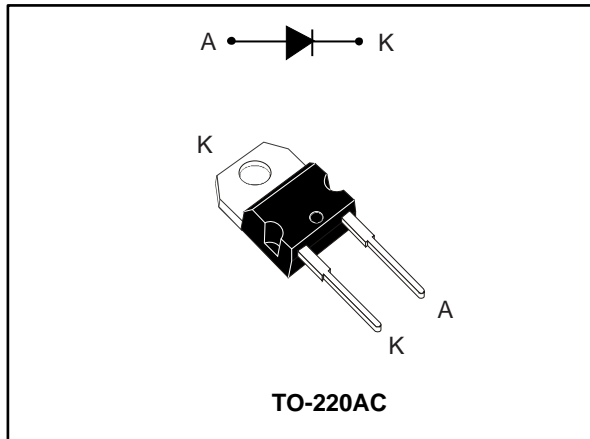


## Automotive 650 V power Schottky silicon carbide diode

Datasheet - production data



### Description

The SiC diode is an ultra high performance power Schottky diode. It is manufactured using a silicon carbide substrate. The wide band gap material allows the design of a Schottky diode structure with a 650 V rating. Due to the Schottky construction, no recovery is shown at turn-off and ringing patterns are negligible. The minimal capacitive turn-off behavior is independent of temperature.

Especially suited for use in PFC applications, this ST SiC diode will boost performance in hard switching conditions. Its high forward surge capability ensures good robustness during transient phases.

### Features

- AEC-Q101 qualified
- No or negligible reverse recovery
- Switching behavior independent of temperature
- Dedicated to PFC applications
- High forward surge capability
- PPAP capable
- ECOPACK® 2 compliant component



Table 1: Device summary

Symbol	Value
$I_{F(AV)}$	12 A
$V_{RRM}$	650 V
$T_j$ (max.)	175 °C

# 1 Characteristics

**Table 2: Absolute ratings (limiting values at 25 °C, unless otherwise specified)**

Symbol	Parameter		Value	Unit
V <sub>RRM</sub>	Repetitive peak reverse voltage		650	V
I <sub>F(RMS)</sub>	Forward rms current		22	A
I <sub>F(AV)</sub>	Average forward current	T <sub>c</sub> = 130 °C <sup>(1)</sup> , δ = 0.5	12	A
I <sub>FRM</sub>	Repetitive peak forward current	T <sub>c</sub> = 110 °C, T <sub>j</sub> = 150 °C, δ = 0.1	50	A
I <sub>FSM</sub>	Surge non repetitive forward current	t <sub>p</sub> = 10 ms sinusoidal, T <sub>c</sub> = 25 °C	100	A
		t <sub>p</sub> = 10 ms sinusoidal, T <sub>c</sub> = 125 °C	90	
		t <sub>p</sub> = 10 μs square, T <sub>c</sub> = 25 °C	400	
T <sub>stg</sub>	Storage temperature range		-55 to +175	°C
T <sub>j</sub>	Operating junction temperature <sup>(2)</sup>		-40 to +175	°C

**Notes:**

<sup>(1)</sup>Value based on R<sub>th(j-c)</sub> max.

<sup>(2)</sup>(dP<sub>tot</sub>/dT<sub>j</sub>) < (1/R<sub>th(j-a)</sub>) condition to avoid thermal runaway for a diode on its own heatsink.

**Table 3: Thermal parameters**

Symbol	Parameter	Value		Unit
		Typ.	Max.	
R <sub>th(j-c)</sub>	Junction to case	1.00	1.4	°C/W

**Table 4: Static electrical characteristics**

Symbol	Parameter	Test conditions		Min.	Typ.	Max.	Unit
I <sub>R</sub> <sup>(1)</sup>	Reverse leakage current	T <sub>j</sub> = 25 °C	V <sub>R</sub> = V <sub>RRM</sub>	-	10	120	μA
		T <sub>j</sub> = 150 °C		-	100	500	
V <sub>F</sub> <sup>(2)</sup>	Forward voltage drop	T <sub>j</sub> = 25 °C	I <sub>F</sub> = 12 A	-	1.56	1.75	V
		T <sub>j</sub> = 150 °C		-	1.98	2.5	

**Notes:**

<sup>(1)</sup>Pulse test: t<sub>p</sub> = 10 ms, δ < 2%

<sup>(2)</sup>Pulse test: t<sub>p</sub> = 500 μs, δ < 2%

To evaluate the conduction losses, use the following equation:

$$P = 1.35 \times I_{F(AV)} + 0.096 \times I_{F(RMS)}^2$$

Table 5: Dynamic electrical characteristics

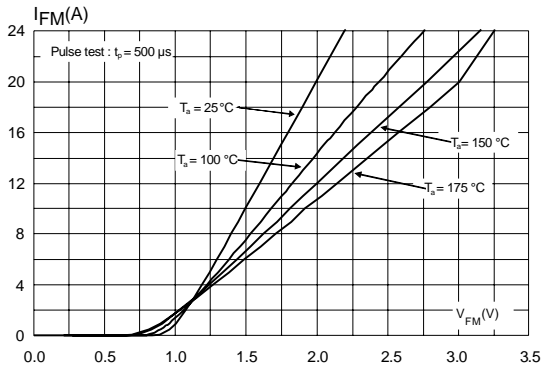
Symbol	Parameter	Test conditions	Typ.	Unit
$Q_{cj}^{(1)}$	Total capacitive charge	$V_R = 400 \text{ V}$	36	nC
$C_j$	Total capacitance	$V_R = 0 \text{ V}, T_c = 25 \text{ }^\circ\text{C}, F = 1 \text{ MHz}$	600	pF
		$V_R = 400 \text{ V}, T_c = 25 \text{ }^\circ\text{C}, F = 1 \text{ MHz}$	60	

**Notes:**

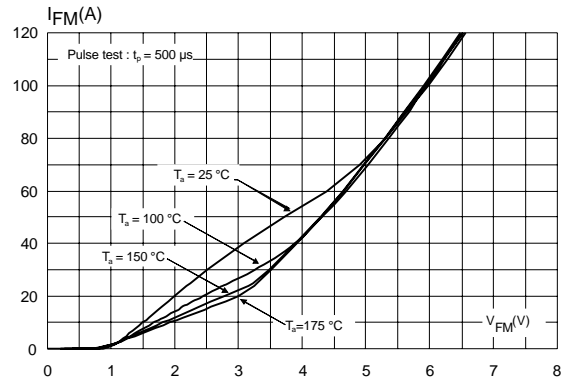
<sup>(1)</sup>Most accurate value for the capacitive charge:  $Q_{cj} = \int_0^{V_{OUT}} C_j(V_R) \cdot dV_R$

# 1.1 Characteristics (curves)

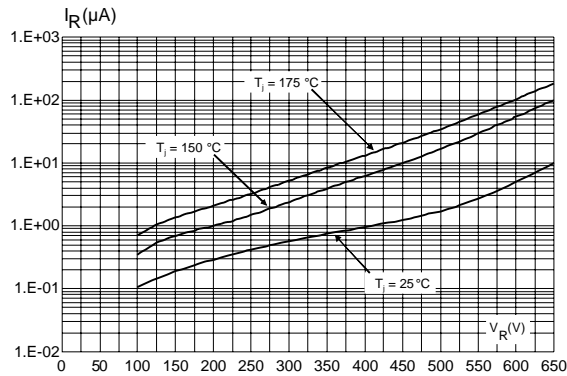
**Figure 1: Forward voltage drop versus forward current (typical values, low level)**



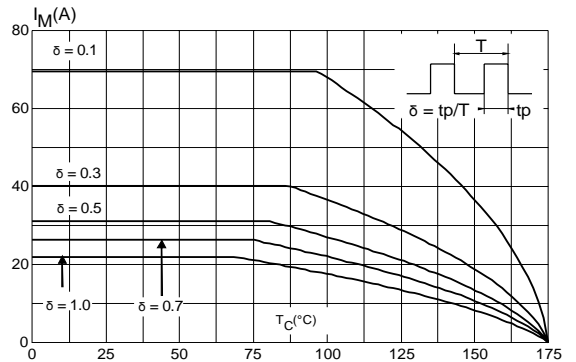
**Figure 2: Forward voltage drop versus forward current (typical values, high level)**



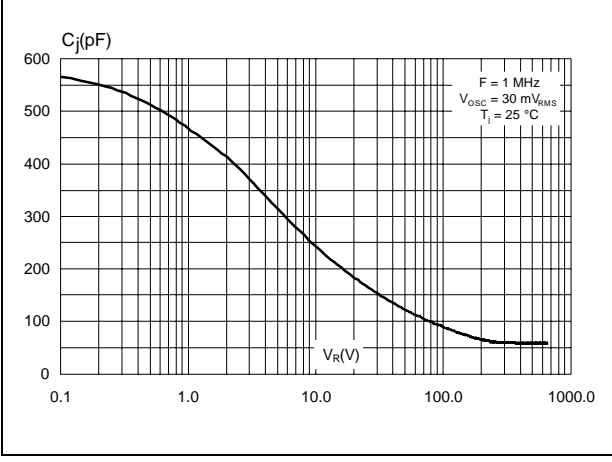
**Figure 3: Reverse leakage current versus reverse voltage applied (typical values)**



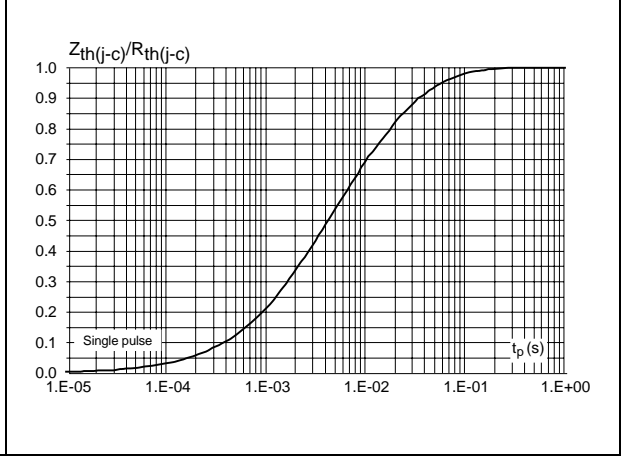
**Figure 4: Peak forward current versus case temperature**



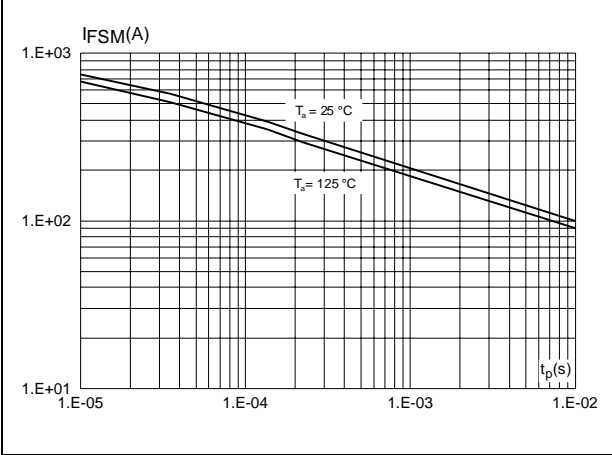
**Figure 5: Junction capacitance versus reverse voltage applied (typical values)**



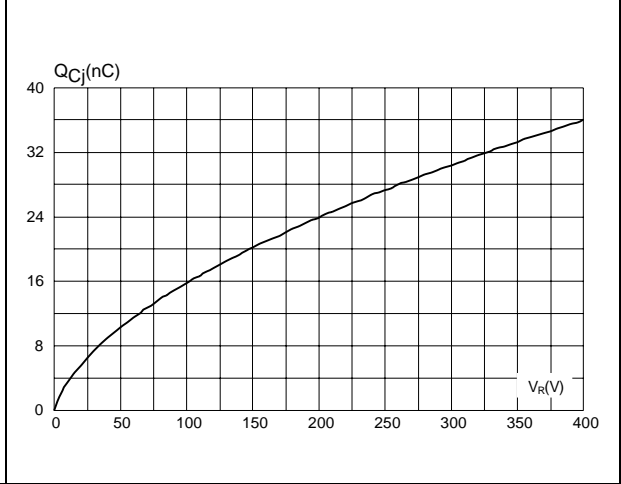
**Figure 6: Relative variation of thermal impedance junction to case versus pulse duration**



**Figure 7: Non-repetitive peak surge forward current versus pulse duration (sinusoidal waveform)**



**Figure 8: Total capacitive charges versus reverse voltage applied (typical values)**



## 2 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: [www.st.com](http://www.st.com). ECOPACK® is an ST trademark.

- Epoxy meets UL94, V0
- Cooling method: by conduction (C)
- Recommended torque value: 0.55 N·m
- Maximum torque value: 0.7 N·m

### 2.1 TO-220AC package information

Figure 9: TO-220AC package outline

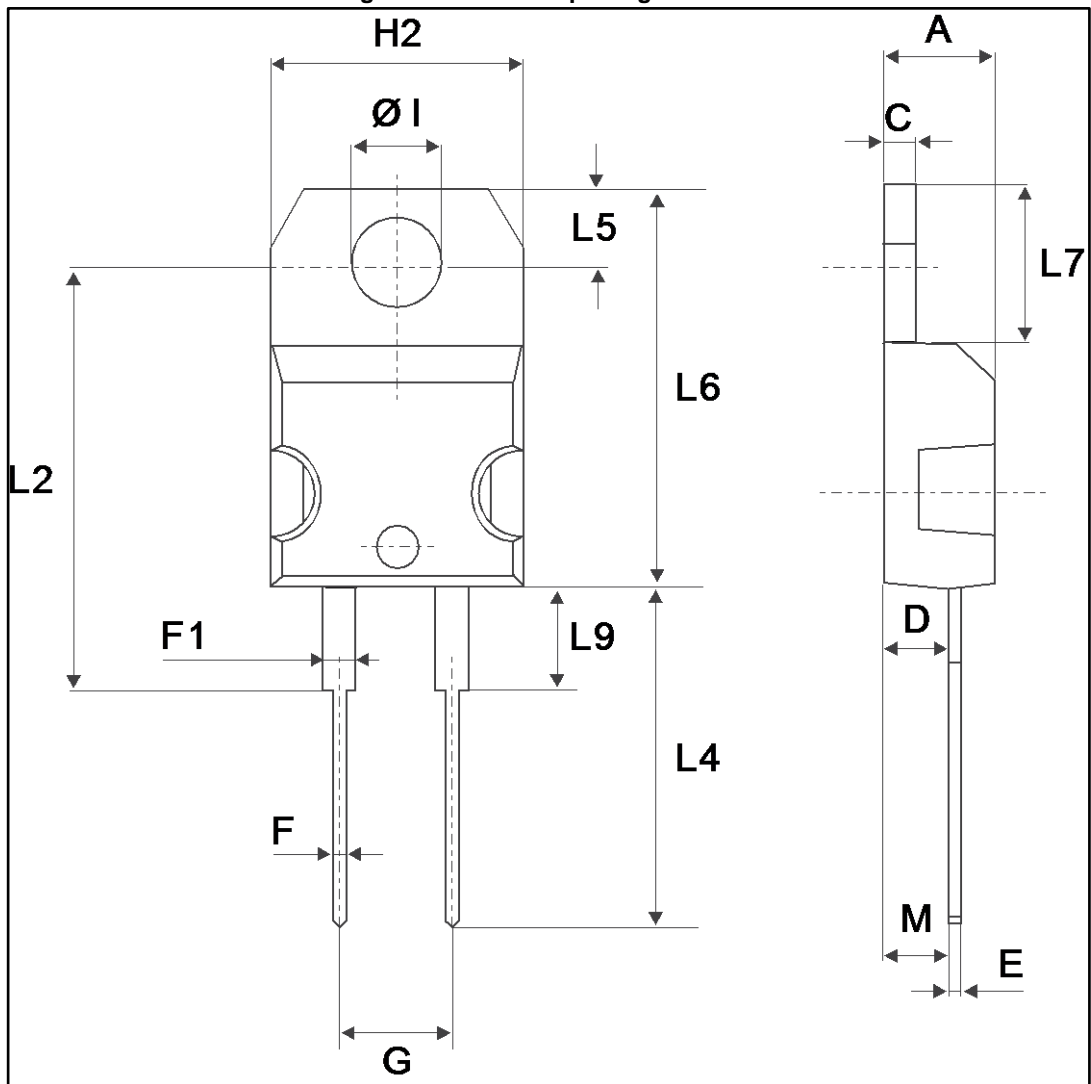


Table 6: TO-220AC package mechanical data

Ref.	Dimensions			
	Millimeters		Inches	
	Min.	Max.	Min.	Max.
A	4.40	4.60	0.173	0.181
C	1.23	1.32	0.048	0.051
D	2.40	2.72	0.094	0.107
E	0.49	0.70	0.019	0.027
F	0.61	0.88	0.024	0.034
F1	1.14	1.70	0.044	0.066
G	4.95	5.15	0.194	0.202
H2	10.00	10.40	0.393	0.409
L2	16.40 typ.		0.645 typ.	
L4	13.00	14.00	0.511	0.551
L5	2.65	2.95	0.104	0.116
L6	15.25	15.75	0.600	0.620
L7	6.20	6.60	0.244	0.259
L9	3.50	3.93	0.137	0.154
M	2.6 typ.		0.102 typ.	
Diam	3.75	3.85	0.147	0.151

### 3 Ordering information

Table 7: Ordering information

Order code	Marking	Package	Weight	Base qty.	Delivery mode
STPSC12H065DY	PSC12H065DY	TO-220AC	1.86 g	50	Tube

### 4 Revision history

Table 8: Document revision history

Date	Revision	Changes
04-Sep-2014	1	First issue.
19-Sep-2014	2	Updated <i>Table 7</i> .
12-Mar-2015	3	Added AEC-Q101 qualified.
24-Oct-2016	4	Updated <i>Table 7</i> : " <i>Ordering information</i> ".



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